



Jim Pappas, Director of Technology Initiatives, Intel

TITLE

The Impact of SNIA's NVM Programming Model

ABSTRACT

The programming model was conceived in SNIA in the expectation new persistent memory (PM) technologies would emerge. Recent announcements have proven such technologies have become viable, and the time for the Model, which is suitable to any specific technology, has come. This panel of developers of the Model will cover:

- The current state of the Model, work in process and future development
- Where and how the Model is and can be deployed
- The impact of PM technologies and the Model on the computing industry

BIOGRAPHY

Jim Pappas is the Director of Technology Initiatives in Intel's Data Center Group. In this role, Jim is responsible to establish broad industry ecosystems that comply with new technologies in the areas of Enterprise I/O, Energy Efficient Computing, and Solid State Storage. Jim has founded, or served on several organizations in these areas including: PCI Special Interest Group, Storage Networking Industry Association (SNIA), InfiniBand Trade Association (IBTA), Open Fabrics Alliance (OFA), The Green Grid, Distributed Management Task Force (DMTF) and several emerging initiatives in his newest focus area of Solid State Storage. Mr. Pappas has previously been the Director of Technology Initiatives in Intel's Desktop Products Group, and successfully led technologies such as AGP Graphics, DVD, IEEE 1394, Instantly Available PC, PCI, USB, and other advanced technologies for the Desktop PC. Mr. Pappas has over 33 years of experience in the computer industry. He has been granted eight U.S. patents in the areas of computer graphics and microprocessor technologies. He has spoken at dozens of major industry events and holds a B.S.E.E. from the University of Massachusetts, Amherst, Massachusetts.