



**Nisha Talagala, VP of Engineering, Parallel Machines**

#### **BIOGRAPHY**

Nisha Talagala is VP of Engineering at Parallel Machines, an early stage startup focused on in-memory distributed machine learning and other advanced analytics. Nisha was previously a Fellow at SanDisk and Fellow/Lead Architect at Fusion-io, where she worked on innovation in non volatile memory technologies and applications. Nisha has more than 15 years of expertise in software development, distributed systems, storage and I/O solutions, persistent memory, and flash. She has worked as technology lead for server flash at Intel - where she led server platform non volatile memory technology development, storage-memory convergence, and partnerships. Prior to Intel, Nisha was the CTO of Gear6, where she designed and built clustered computing caches for high performance I/O environments. Nisha also served at Sun Microsystems, where she developed storage and I/O solutions and worked on file systems. Nisha earned her PhD at UC Berkeley where she did research on clusters and distributed storage. Nisha holds more than 35 patents in distributed systems, networking, storage, performance and non-volatile memory.