



Terry Hullett, VP, Systems Engineering & GM Systems Solutions Group, Everspin

BIOGRAPHY

Terry Hulett is Vice President, Systems Engineering & GM Systems Solutions Group. Terry and his team are responsible for growing Everspin's capabilities and business strategy as a world class solutions provider, developing the expended memory ecosystem for spin torque MRAM, and accelerating the success of customers integrating ST-MRAM in their storage and memory applications. Previously, as a Strategic Planner at Intel, he was responsible for strategies for high end networking products and server class CPUs. Terry has a background in Architecture, HW & SW Development and Operations on products like leading edge network controllers and leading edge CPUs. He was a member of the original 68000 design team at Motorola. Prior to Intel he held several Engineering Executive roles at NetEffect, Banderacom, Si Solutions and AMD and is widely respected for building and leading product delivery teams. Terry holds several patents and published several technical articles.