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TITLE
Migrating Compute into the Storage Enclosure

ABSTRACT

NVMe & NVMe-over-Fabrics have unfurled the full performance and parallelism of solid state drivers. The trends towards both disaggregated storage and Hyperconverged systems offer two very distinct approaches to providing reconfigurable storage acceleration in the datacenter and the enterprise. We take an in-depth look into technologies needed to move compute from the CPU to the storage sub-system for these two key datacenter architectures.   We will share an update on the ongoing R&D work at Xilinx and its partners, investigating both production implementations and proof-of-concept prototype

BIOGRAPHY

Rakesh Cheerla is currently Product Manager for Storage Solutions at Xilinx, where he focuses on data center design, hardware management, and open storage standards. He was previously Sr Director Products at CNEX Laboratories, a startup developing flash controller chips, where he was in charge of determining customer requirements and defining products to meet them. Before joining CNEX, he worked at SMART Modular Systems, LSI, and Extreme Networks. He has focused on developing product requirements, defining features, and managing engineering teams. He holds an MBA from Columbia University and an MSEE from Arizona State University.