

STORAGE VISIONS® 2018

October 22-23, 2018 at the Hyatt Regency, Santa Clara, CA

AN ENTERTAINMENT STORAGE ALLIANCE™ EVENT



ENTERTAINMENT
STORAGE
ALLIANCE™



Justin Wenck, Senior Technical Marketing Engineer, PCI-SIG

TITLE: PCI Express: What's Next for Storage

ABSTRACT

PCI Express® (PCIe®) 3.0 architecture has enabled Flash Storage to transition to high speed, low latency power efficient performance over the past few years. However, the demand for additional performance in power constrained devices and data-hungry applications like IoT continues. In anticipation of these emerging data demanding markets, PCI-SIG® continues its nearly three decade history of delivering performance doubling with the development of the PCIe 4.0 and PCIe 5.0 specifications.

This presentation will review the major features of PCIe 4.0 and PCIe 5.0 technology, which will continue to enable power efficient performance required as NAND capacities scale and faster SCM (Storage Class Memories) become mainstream. Session attendees will gain insight into the current status of the PCIe 4.0 technology rollout and testing and will learn about the PCIe 5.0 specification development and the timeline for completion in 2019.

BIOGRAPHY

Justin Wenck, Ph.D. is a Senior Technical Marketing Engineer in the Datacenter SSD Technical Marketing Group at Intel Corporation responsible for PCIe NVMe SSDs. He has also worked as a validation lead ensuring SSDs were PCI-SIG compliant, and a circuit designer for memory interfaces. Dr. Wenck holds Ph.D. and M.S. degrees in electrical and computer engineering from the University of California-Davis and a B.S. in electrical engineering from California Polytechnic State University, San Luis Obispo.